

a substantially planar main body; and

a first storage portion provided on a first surface of said main body for storing the semiconductor integrated circuit device, said first storage portion having a bottom surface and a first wall surface extending from said bottom surface and arranged around the semiconductor integrated circuit device when the semiconductor integrated device is stored in said first storage portion,

a second wall surface disposed around a circumference of the semiconductor integrated circuit device so as to limit horizontal movement of the semiconductor integrated circuit device, said first wall surface being inclined at an angle so as to support an edge of the package of the semiconductor integrated circuit device such that the wiring terminals of the semiconductor integrated circuit device do not contact said first wall surface when the semiconductor integrated circuit device is stored in said first storage portion, and said second wall surface extending from said first wall surface in a direction away from said first wall surface of said main body, wherein said second wall surface is inclined at an angle larger than the angle of said first wall surface, with respect to the horizontal.

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13. (Amended) A tray for storing a semiconductor integrated circuit device having a package and wiring terminals on a lower surface of the package, said tray comprising:

a substantially planar main body;

a first storage portion provided on a first surface of said main body for storing the semiconductor integrated circuit device, said first storage portion having a first wall surface adapted to be arranged around the semiconductor integrated circuit device when the semiconductor integrated device is stored in said first storage portion, wherein said first wall surface has a first area which is inclined at an angle so as to support an edge of the package of the semiconductor integrated circuit device and to prevent said first wall surface from coming into contact with the wiring terminals of the semiconductor integrated circuit device when the semiconductor integrated circuit device is stored in said first storage portion; and

a second storage portion provided on a second surface of said main body opposite to said first storage portion, wherein said second storage portion can store a semiconductor integrated circuit device with wiring terminals thereof facing upward when said tray is turned over, and

wherein when two of said trays are aligned in a stacked relationship said second storage portion of one tray cooperates with said first storage portion of the other tray to form a space for storing the semiconductor integrated circuit device, and when said two aligned trays are not turned over, the one of said two trays which is positioned on top, does not make contact with a stored semiconductor integrated circuit device.

16. (Amended) A tray according to claim 15, wherein said main body includes a plurality of projecting pieces provided on said second surface thereof for defining said second storage portion, and wherein each of said projecting pieces has a wall surface for serving as said

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second wall surface, whereby said wall surfaces of said projecting pieces respectively support corners of the rectangular package of the semiconductor integrated circuit device, when said tray is in a turned-over state.

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